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**DECLARATION OF JOINT INVENTORS FOR PATENT APPLICATION**

As the below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name.

I believe I am the original, first and joint inventor of the subject matter which is claimed and for which a patent is sought on the invention entitled: Methods of Enhancing Data Retention of a Floating Gate Transistor, Methods of Forming Floating Gate Transistors, and Floating Gate Transistors, the specification of which is attached hereto.

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims.

I acknowledge the duty to disclose information known to me to be material to patentability as defined in Title 37, Code of Federal Regulations §1.56.

**PRIOR FOREIGN APPLICATIONS:**

I hereby state that no applications for foreign patents or inventor's certificates have been filed prior to the date of execution of this declaration.

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful

false statements may jeopardize the validity of the application or any patent issued therefrom.

\* \* \* \* \* \* \* \*

Full name of inventor: J. Dennis Keller

Inventor's Signature: Dennis Miller

Date: 7/1/98

**Residence:** **Boise, Idaho**

**Citizenship:** U.S.A.

**Post Office Address:** 1863 S. Londoner Way  
Boise, ID 83706

\* \* \* \* \* \* \* \* \*

Full name of inventor: **Roger R. Lee**

Inventor's Signature: *b. sande*

Date: 7-1-98

Residence: **Boise, Idaho**

**Citizenship:** U.S.A.

Post Office Address: ~~335 Raindrop  
Boise, ID 83706~~

3936 N. BURNS ROAD  
BURE 113 83704

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Inventor . . . . . J. Dennis Keller et al.  
Assignee . . . . . Micron Technology, Inc.  
Attorney's Docket No. . . . . MI22-587  
Title: Methods of Enhancing Data Retention of a Floating Gate Transistor, Methods  
of Forming Floating Gate Transistors, and Floating Gate Transistors

**POWER OF ATTORNEY BY ASSIGNEE AND  
CERTIFICATE BY ASSIGNEE UNDER 37 CFR §3.73(b)**

To: Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

MICRON TECHNOLOGY, INC., the Assignee of the entire right, title and interest in the above-identified patent application by assignment attached hereto, hereby appoints the attorneys and agents of the firm of WELLS, ST. JOHN, ROBERTS, GREGORY & MATKIN P.S., listed as follows:

Richard J. St. John	Reg. No. 19,363
David P. Roberts	Reg. No. 23,032
Randy A. Gregory	Reg. No. 30,386
Mark S. Matkin	Reg. No. 32,268
James L. Price	Reg. No. 27,376
Deepak Malhotra	Reg. No. 33,560
Mark W. Hendrickson	Reg. No. 32,356
David G. Latwesen	Reg. No. 38,533
George G. Grigel	Reg. No. 31,166
Keith D. Grzelak	Reg. No. 37,144
John S. Reid	Reg. No. 36,369
Lance R. Sadler	Reg. No. 38,605
James D. Shaurette	Reg. No. 39,833

and also attorneys Michael L. Lynch (Reg. No. 30,871) and Lia Pappas Dennison (Reg. No. 34,095) of Micron Technology, Inc., as its attorneys with full power of substitution to prosecute this application and transact all business in the Patent and Trademark Office connected therewith.

1                          The Assignee certifies that the above-identified Assignment has  
2 been reviewed and to the best of Assignee's knowledge and belief, title  
3 is in the Assignee.

4                          Please direct all correspondence regarding this application to:

5                          Wells, St. John, Roberts, Gregory & Matkin P.S.  
6                          Attn: Mark S. Matkin  
6                          601 W. First Avenue, Suite 1300  
7                          Spokane, WA 99201-3817

7                          Telephone: (509) 624-4276  
8                          Facsimile: (509) 838-3424

9                          MICRON TECHNOLOGY, INC.

10                         Dated: 7/13/98                          By: MLL  
11                         Name: Michael L. Lynch  
12                         Title: Chief Patent Counsel